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## VisualRoute 2010 Pro V14.0g Build 4763 -S2 [NEW]



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. Comment . out until the desired depth is reached. The procedure is repeated until the entire semiconductor wafer is uniformly coated with the colloidal particles or alternatively a first layer of oxide is deposited. The silicon oxide deposition can be accomplished by passing an oxygen gas through a showerhead mounted above the wafer and thereby dispersing the oxygen atoms into the stream of helium gas to form a colloidal gas. The resulting oxygen atoms collide with the surface of the wafer to deposit an initial layer of oxide. The silicon dioxide layer is then reacted with an oxidizing agent, such as oxygen or ozone, to form the desired oxide. The reaction is self-limiting and the film is grown until the thickness of the layer is equal to that of the underlying layer. To remove the excess oxide growth material from above the wafer or remove a portion of the initial oxide layer, reactive ion etching is carried out with an oxygen plasma. To make clean or wet-etching of the wafer, the wafer is immersed in a chemilicent bath or is dipped into, or rinsed with, a wet chemical solution. The etching solution can be of the type to etch silicon, such as phosphoric acid or acetic acid or hydrofluoric acid, or of the type to etch silicon nitride, such as ammonia, potassium hydroxide, or ethylene diamine. The semiconductor wafer is then heated, in one embodiment, for approximately 30 seconds at a temperature of about 1,050° C. and for approximately 30 minutes at about 1,050° C. The semiconductor wafer is then cooled to room temperature at approximately 0.5° C./sec or at a temperature of less than about 200° C. D. Heat Retention/Buffer Ring Technique In accordance with another aspect of the present invention, a thermal isolation layer can be added between the substrate and the material grown on the semiconductor wafer. The thermal isolation layer can be composed of polysilicon, silicon nitride, silicon oxynitride, silicon carbide, silicon oxycarbide, or any other material which is able to provide a high degree of thermal isolation from the underlying substrate. Typically, the thermal isolation layer has a higher specific heat (heat capacity) than the substrate, or in certain embodiments, the thickness of the thermal isolation layer is greater than the height of the device structures. The thermal isolation layer can be adhered to the substrate by a 0cc13bf012

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